3　説明書の交付　本公表に基づき応募する供給者に対して導入説明書を交付する。
⑴　交付期間　令和８年３月11日から令和８年４月21日まで。
⑵　交付場所　上記２⑵に同じ。
4　説明会の開催　本公表に基づく導入説明会を開催する。
⑴　開催日時　令和８年３月24日14時00分
⑵　開催場所　Zoomによるオンライン説明会
5　その他　この導入計画の詳細は導入説明書による。なお、本公表内容は予定であり、変更することがあり得る。
6　Summary
⑴　Classification of the products to be procured : 14
⑵　Nature and quantity of the products to be purchased : Integrated Infrastructure System for Simulation, Data, Learning, and Inference 1 Set
⑶　Type of the procurement : Purchase
⑷　Basic requirements of the procurement :
A　The system shall meet the following hardware requirements.
①　The computing system shall consist of compute nodes composed of accelerator nodes equipped with computational accelerators and general-purpose CPU nodes.
②　The total theoretical peak performance of the accelerator nodes (by double-precision floating-point arithmetic) shall be 140PFLOPS or higher. Emulation is also acceptable.
③　The total theoretical AI computation performance of the accelerator nodes (by FP4 or higher-precision floating-point arithmetic, without sparsity consideration) shall be 25EFLOPS or higher.
④　The total memory bandwidth of the accelerator nodes shall be 1.5PByte／sec or higher, and the total memory capacity shall be 200TByte or higher.
⑤　The CPUs of the general-purpose CPU nodes shall collectively provide 6,000 or more cores. The theoretical peak performance (double-precision floating-point arithmetic) per core shall be 65GFLOPS or higher, memory capacity per core 2.0GByte or higher, and memory bandwidth per core 5.0GByte／sec or higher. The instruction set architecture of the general-purpose CPU nodes may differ from that of the accelerator nodes.
⑥　Each compute node shall be equipped with an NVMe SSD with 1.5TByte or greater physical capacity.
⑦　The inter-node network interface of each compute node shall support a bandwidth of 400Gbps or higher per accelerator device on accelerator nodes. For general-purpose CPU nodes, the bandwidth shall be 400Gbps or higher per node.
⑧　Each general-purpose CPU node shall be capable of communicating directly with external systems with an aggregate bandwidth of 800Gbps or higher. Each accelerator node shall communicate with external systems via the general-purpose CPU nodes.
⑨　A highly reliable parallel file system with a storage capacity of 5.0PByte or more shall be provided. It shall allow expansion to approximately 20PByte without reconfiguration while maintaining the same configuration. It shall support data compression. The compute nodes shall be able to access the file system with a transfer performance of 1.0TByte／sec or higher.
B　The system shall meet the following software requirements.
①　The system shall run the Linux operating system.
②　The system shall provide compilers for Fortran 2008, C11, and C＋＋17 or later for general-purpose CPUs, supporting automatic SIMD vectorization and the OpenMP API version 4.5 or higher. For accelerator devices, it shall provide compilers for Fortran 2008, C11, and C＋＋17 or later with automatic parallelization, the OpenACC API version 2.7 or higher, and the OpenMP API version 5.0 or higher.
③　A communication library supporting MPI version 3.1 or higher shall be provided.
④　A Python runtime environment shall be provided.
⑤　Highly optimized numerical computation libraries and machine-learning libraries shall be provided.
⑥　A batch job system shall be provided. A single job shall be able to use both the general-purpose CPU node group and the accelerator node group simultaneously.
⑦　Kubernetes shall be provided as the container system.
C　The total power consumption of the installed system, excluding cooling equipment, shall be 2.0MVA or less. Power capacity, cooling, and installation design shall ensure sufficient heat dissipation for continuous operation of CPUs, accelerator devices, memory, storage devices, and network switches. Direct water cooling shall be used for general-purpose CPUs, accelerator devices, and inter-node network switches. Operational costs for other components such as storage shall be reduced through rear-door heat-exchanger water cooling or equivalent methods. The installation area, excluding cooling equipment, shall be 45 square meters or less.
⑸　Time limit for the submission of the requested material : 17 : 00　21 April, 2026
⑹　Contact point for the notice : WADA Kazuhiro, Accounting Team, Information Strategy Group, Information Systems Department, The University of Tokyo, 6―2―3 Kashiwanoha Kashiwa-shi Chiba-ken 277―0882 Japan, TEL 070―1531―4283

資料提供招請に関する公表

次のとおり物品の導入を予定していますので、当該導入に関して資料等の提供を招請します。
令和８年３月11日
国立大学法人京都大学長　湊　　長博
◎調達機関番号　415　◎所在地番号　26
○第12号
1　調達内容
⑴　品目分類番号　22、31
⑵　導入計画物品及び数量
３テスラＭＲＩシステム　一式
⑶　調達方法　購入等
⑷　導入予定時期　令和８年度２月以降
⑸　調達に必要とされる基本的な要求要件
Ａ　ＭＲＩシステムは３テスラの静磁場強度を備えていること。
Ｂ　脳撮像に特化した撮像用コイルを有し、高速で高画質な画像を得られること。
Ｃ　高い磁場均一性および高い傾斜磁場性能を有すること。
Ｄ　得られた画像を画像（ＤＩＣＯＭ）サーバにて保存できること。
Ｅ　検査を安全かつ効率的に遂行するための周辺機器（画像処理装置など）を備えること。
2　資料及びコメントの提供方法　上記１⑵の物品に関する一般的な参考資料及び同⑸の要求要件等に関するコメント並びに提供可能なライブラリーに関する資料等の提供を招請する。
⑴　資料等の提供期限　令和８年４月27日17時00分（郵送の場合は必着のこと。）
⑵　提供先　〒606―8501 京都市左京区吉田本町36番地１　京都大学本部構内（文系）共通事務部経理課　外部資金掛長　福元　　隆　電話075―753―6905
3　説明書の交付　本公表に基づき応募する供給者に対して導入説明書を交付する。
⑴　交付期間　令和８年３月11日から令和８年４月27日まで。
⑵　交付場所　上記２⑵に同じ。
4　説明会の開催　本公表に基づく導入説明会を開催する。
⑴　開催日時　令和８年３月25日14時00分
⑵　開催場所　京都大学稲盛財団記念館２階225会議室