動並列化機能及びOpenACC　ＡＰＩ（バージョン2.0以上）を有するFortran90、C／C＋＋の処理系を備えること。

ｃ　ＭＰＩ3.0通信ライブラリが提供されること。

ｄ　高度に並列化された数値計算ライブラリが提供されること。

ｅ　バッチジョブシステムが提供されること。

Ｃ　導入システム全体の消費電力は、空調設備の電力を除いて500kVA以下であること。ＣＰＵ、メモリおよびディスク装置が連続的に稼働し続けた際にも十分な廃熱が行えるよう、電源容量、空調、設置方式が考慮されること。設置面積は空調設備を除いて90平米以下であること。

2　資料及びコメントの提供方法　上記1⑵の物品に関する一般的な参考資料及び同⑹の要求要件等に関するコメント並びに提供可能なライブラリーに関する資料等の提供を招請する。

⑴　資料等の提供期限　平成27年９月24日17時00分（郵送の場合は必着のこと。）

⑵　提供先　〒113―8658　東京都文京区弥生２―11―16　東京大学情報システム部情報戦略課会計チーム　平原　康道　電話03―5841―2707

3　説明書の交付　本公表に基づき応募する供給者に対して導入説明書を交付する。

⑴　交付期間　平成27年８月11日から平成27年９月24日まで。

⑵　交付場所　上記2⑵に同じ。

4　説明会の開催　本公表に基づく導入説明会を開催する。

⑴　開催日時　平成27年８月18日14時00分

⑵　開催場所　東京大学情報基盤センター本館１階遠隔講義室(103)

5　その他　この導入計画の詳細は導入説明書による。なお、本公表内容は予定であり、変更することがあり得る。

6　Summary

⑴　Classification of the products to be procured : 14

⑵　Nature and quantity of the products to be rent : Integrated Supercomputer System for Data Analysis and Scientific Simulations 1 set

⑶　Type of the procurement : Rent

⑷　Basic requirements of the procurement :

A　"Integrated Supercomputer System for Data Analysis and Scientific Simulations" must satisfy the following hardware specifications :

a　The supercomputer system must be composed of the general purpose nodes which consist of a general purpose CPU part and the computation accelerator nodes which consist of a computation accelerator part in addition to a general purpose CPU part. The computation accelerator nodes must be equal to or more than one-fifth of the computation nodes in the entire system

b　The theoretical peak performance on double-precision floating point operation of the general purpose CPU part in each node must be 900 GFLOPS or higher, and the total performance obtained by the general purpose CPU parts of the system must be 500 TFLOPS or higher. The total performance obtained by the computation accelerator parts of the system must be 960 TFLOPS or higher

c　The general purpose CPU part of a computation node consists of multiple CPU cores connected by a hardware shared memory system

d　The general purpose CPU must be based on the 64-bit extended IA32 architecture

e　The general purpose CPU part on each node must have the main memory of 256 GiByte or more, and must be the memory bandwidth of 150 GiB／s or more

f　The computation accelerator part must be constructed by a set of multiple computation cores connected by a hardware shared memory system. This part must be connected to the general purpose CPU part to perform high speed computing coordinating with it

g　The computation accelerator part on each computation accelerator node must have the main memory of 32 GiByte or more. It is allowed to provide the same capacity with multiple devices

h　On a computation accelerator node, the general purpose CPU part and the computation accelerator part must be connected by a bus system with equal to or more than 15.7 GB／s of theoretical peak bandwidth

i　The theoretical peak bandwidth of the network interface in each node must be 100 Gbps or higher. It is allowed to support the same bandwidth with mutiple interfaces

j　On a computation accelerator node, the direct communication between nodes for the data on the main memory of the computation accelerator device must be supported with no memory copy via the main memory of the general purpose CPU

k　All nodes are connected in full bisection bandwidth

l　A global file system must be a parallel file system with highly reliable equal to or more than 2 PByte storage. It must allow the read and write operation from the computation nodes with equal to or more than 40 GB／s bandwidth

m　A fast filesystem cache must be a file system using non-volatile solid-state memory device with equals to or more than 150TB. It must allow the read and write operation from the computation nodes with equal to or more than 200 GB／s bandwidth

B　"Integrated Supercomputer System for Data Analysis and Scientific Simulations" must satisfy the following software specifications :

a　A Linux operating system must run

b　On the general purpose CPU part of each computation nodes, Fortran 90, C and C＋＋ languages must be supported with automatic parallelization function and OpenMP API (Version 3.0 or higher). On the computation accelerator part, Fortran 90, C and C＋＋ languages must be supported with automatic parallelization function and OpenACC API (Version 2.0 or higher)

c　An MPI 3.0 library must be provided

d　Highly parallel math libraries must be provided

e　A batch job system must be provided

C　Overall maximum power consumption except the cooling system must be 500 kVA or less. The air flow and system assembly are carefully designed so that the system is kept cool even if CPU, memory, and disks are fully and continuously operated. The footprint of entire system except cooling system must be equal to or less than 90 square meters

⑸　Time limit for the submission of the requested material : 17 : 00　24 September, 2015

⑹　Contact point for the notice : Yasumichi Hirahara, Accounting Team, Information Strategy Group, Information Systems Department, The University of Tokyo, 2―11―16 Yayoi Bunkyo-ku, Tokyo 113―8658 Japan, TEL 03―5841―2707

意見招請に関する公示

次のとおり調達役務の仕様書案の作成が完了したので、仕様書案に対する意見を招請します。

平成27年８月11日

総務省統計局長　會田　雅人

◎調達機関番号　012　◎所在地番号　13

1　調達内容

⑴　品目分類番号　72

⑵　購入等件名及び数量　平成28年経済センサス―活動調査　コールセンター業務　一式

2　意見の提出方法

⑴　意見の提出期限　平成27年９月１日17時迄（郵送の場合は必着のこと。）